

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	144898	(via via\$hole through\$hole through adj hole interposer pin BGA pad) same (locat\$4 arrang\$5 re\$arrang\$5 distribut\$4) same (wiring interconnect\$4 routing bond)	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:12
L2	34014	(compensat\$4 adjust\$4 rotation) same ("IC" chip semiconductor) same (position location)	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:13
L3	2341	L1 and L2	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:13
L4	71	L3 and routing near10 channel	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:14
L5	1	L4 and (716/1,8-15.ccls. 438/6,25,26,106-113.ccls.)	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:18
L6	148	L3 and (716/1,8-15.ccls. 438/6,25,26,106-113.ccls.)	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:18
L7	101	L6 and @ad<= "20041101"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:20
L8	17	L7 and "BGA"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:20
L9	1422676	(via via\$hole through\$hole through adj hole interposer pin BGA pad) same (geometr \$4 location position)	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:22

L10	94	L7 and L9	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:23
L11	32	L10 and bond near wire	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:23
L12	18	L10 and (design near5 rule)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 11:24

1/7/2009 12:25:41 PM

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